ABSTRACT OF THE DISCLOSURE

A system and method uses a debug card to access to PCI bus data. The debug card includes a PCI interface, a storage module, a data control chip and a host interface. First, the PCI interface of the debug card is used to access to the PCI bus data. The accessed data are stored in a buffer of the debug card. The data control chip of the debug card then is used to control the access to the data stored in the buffer of the debug card. The accessed data are stored in a buffer of the data control chip. Lastly, the host interface of the debug card is used to output the data stored in the buffer of the data control chip.

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